

FIG. 1 is a block diagram of a video image processing system. The system includes a plurality of cameras (1-1, 1-2, ..., 1-N) connected to a central processing unit (3). Each camera outputs a video signal (VIDEO SIGNAL 1, VIDEO SIGNAL 2, ..., VIDEO SIGNAL N) to a corresponding capture board (2-1, 2-2, ..., 2-N). The capture boards are connected to a common bus (3), which is also connected to a processor (4), work memory (5), and a display board (6). The display board is connected to a display (8). A dictionary (7) is also connected to the processor (4).

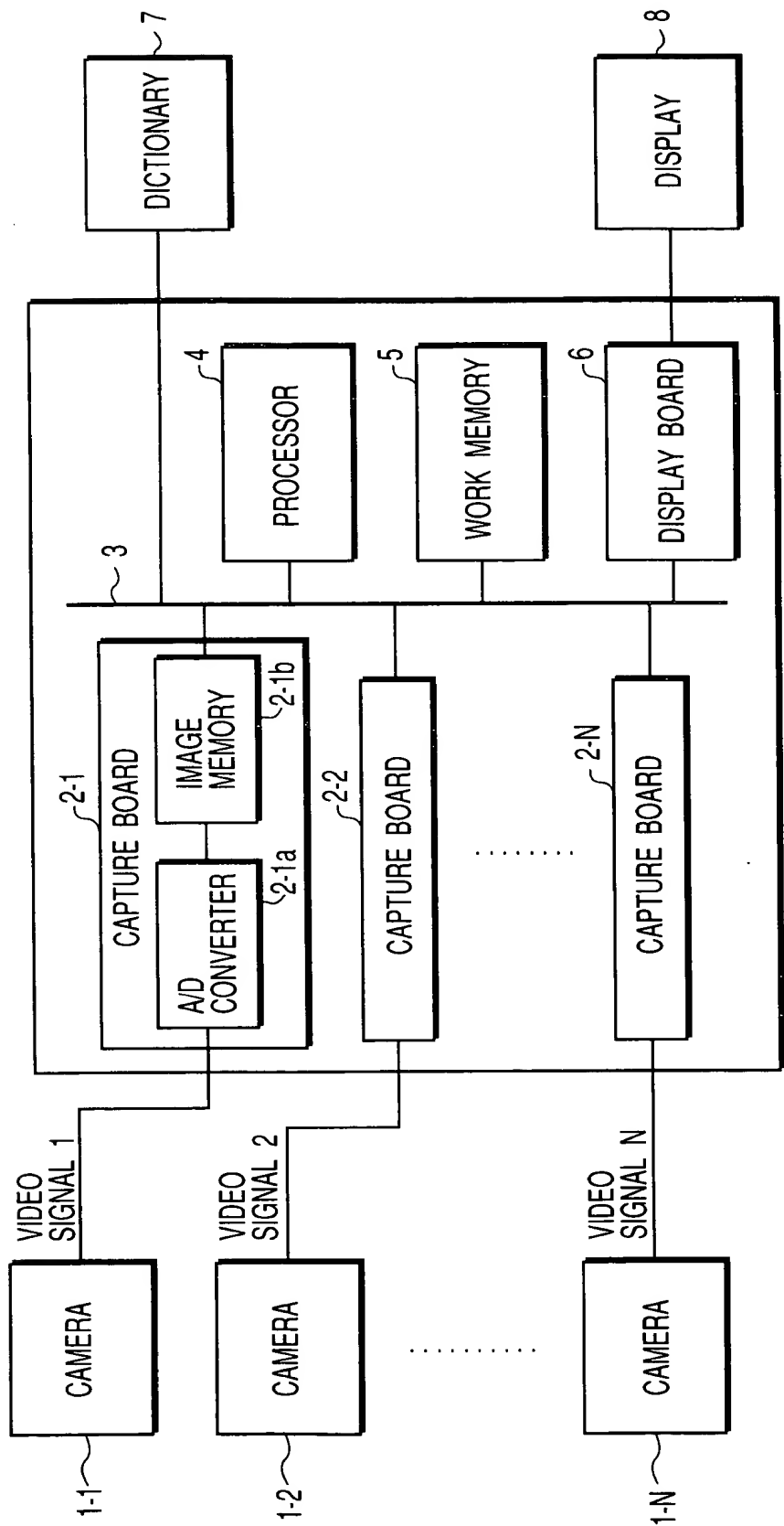


FIG. 1

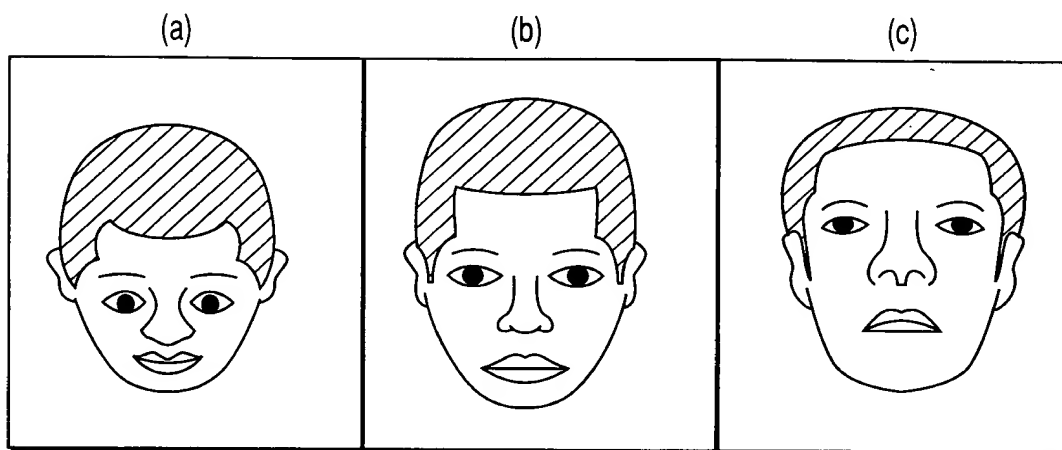
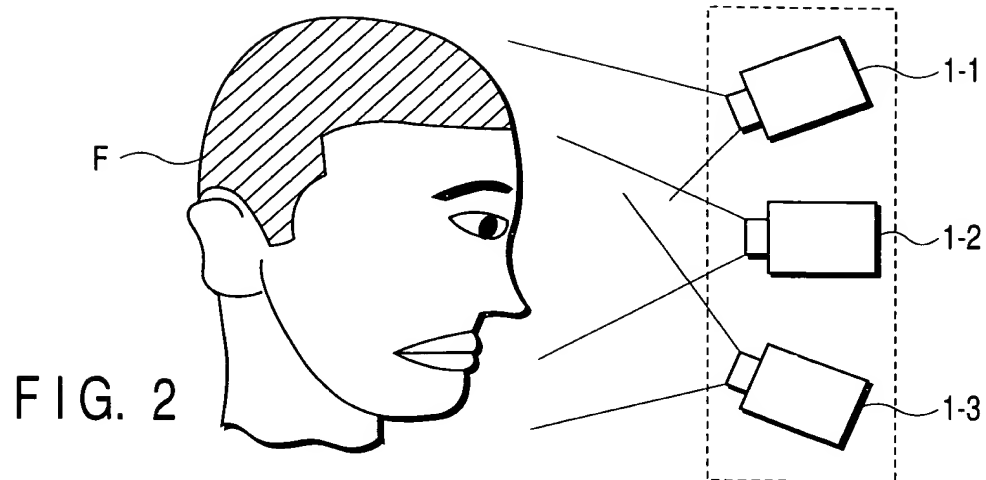
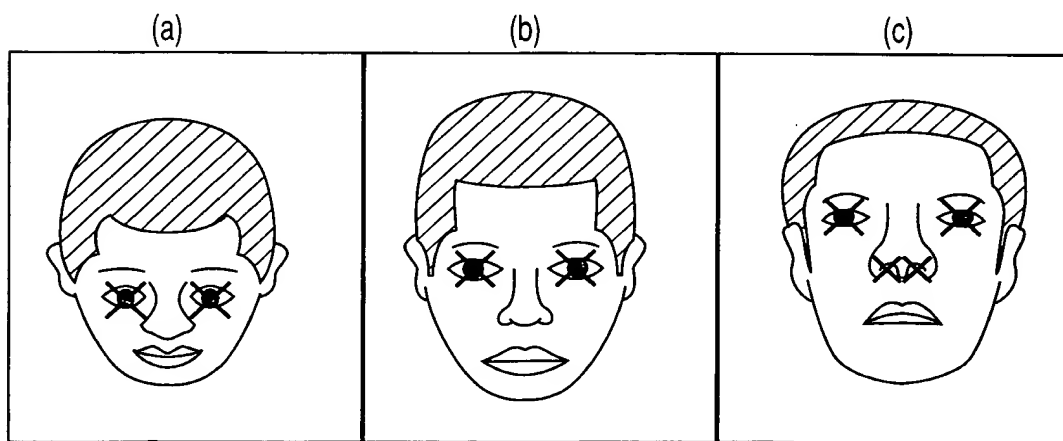


FIG. 3



X: FEATURE POINT

FIG. 5

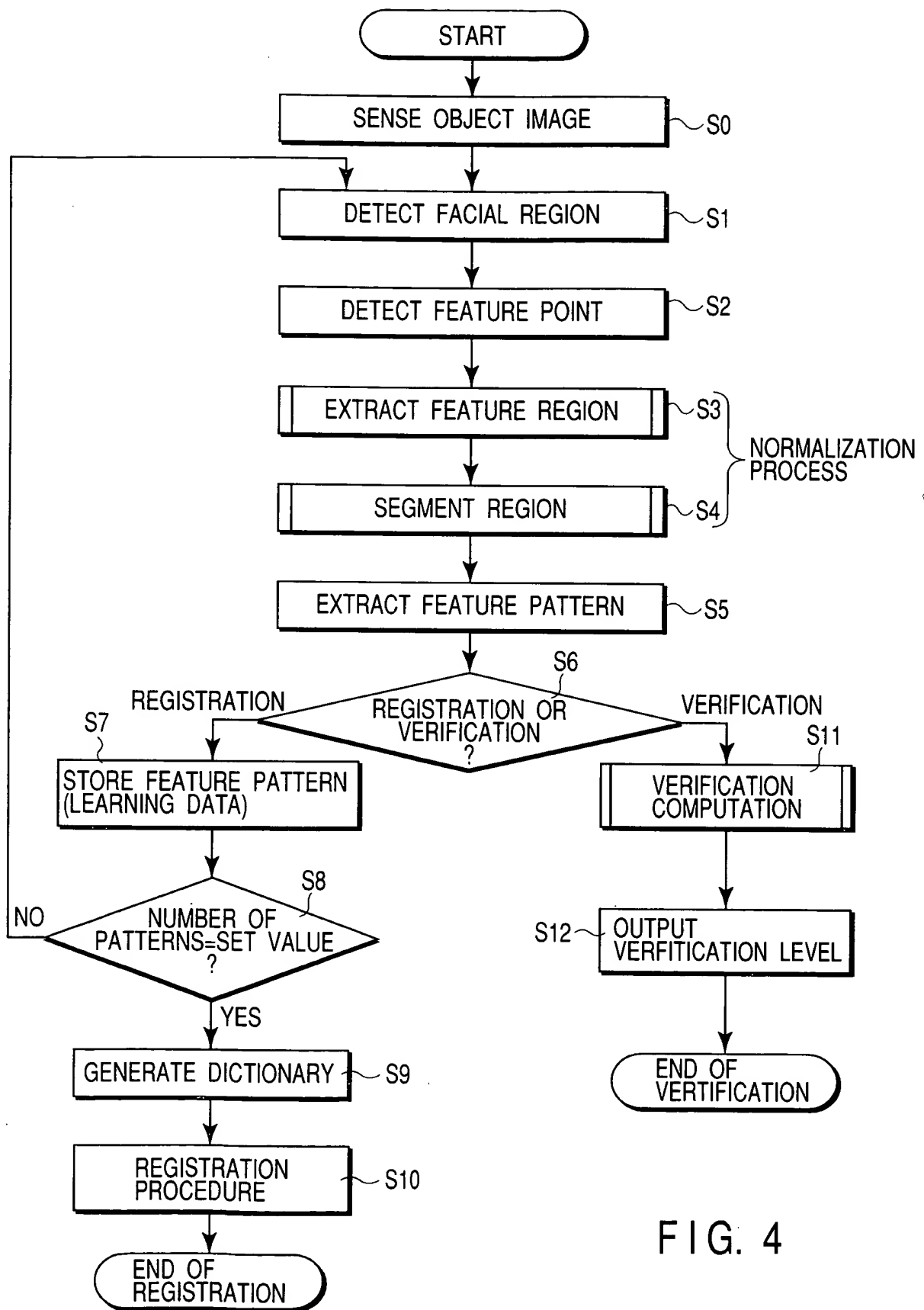


FIG. 4

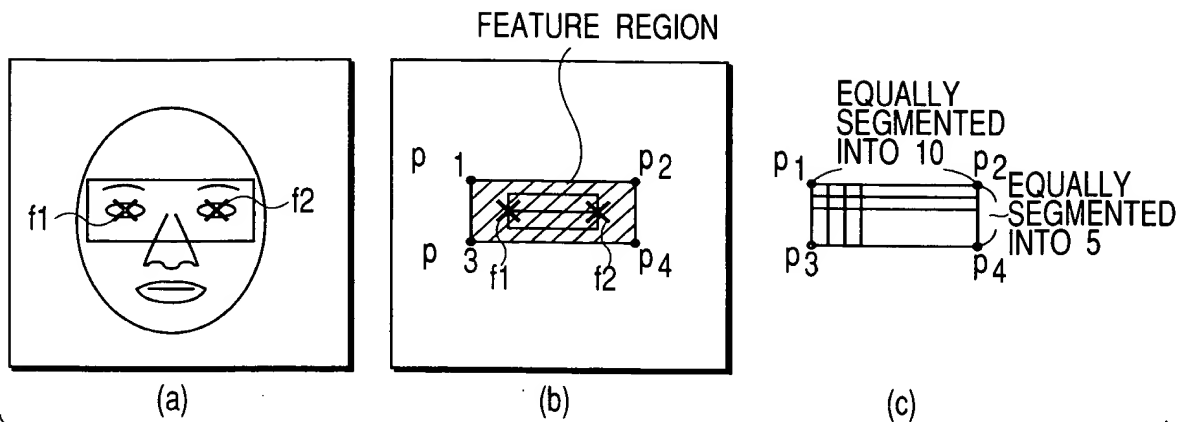


FIG. 6

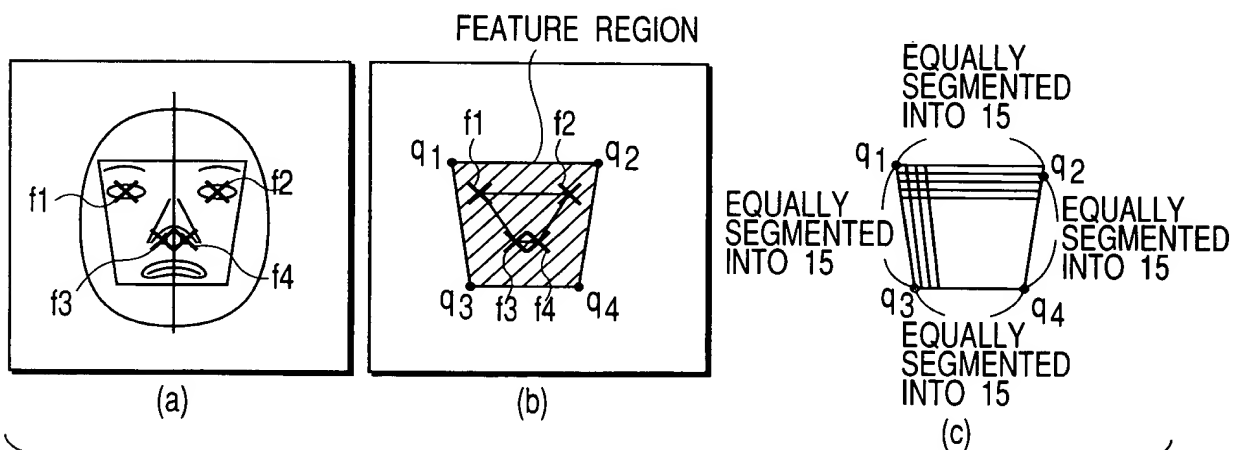


FIG. 7

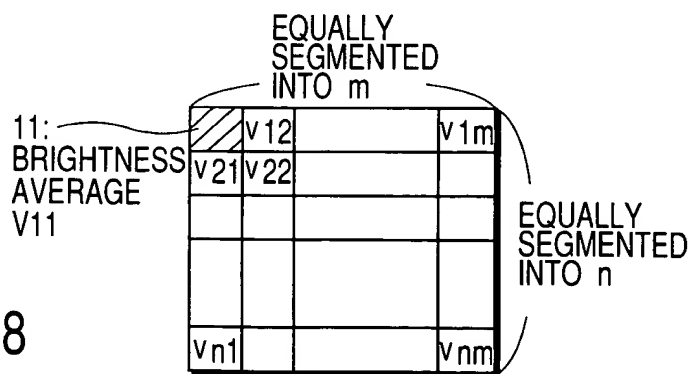


FIG. 8

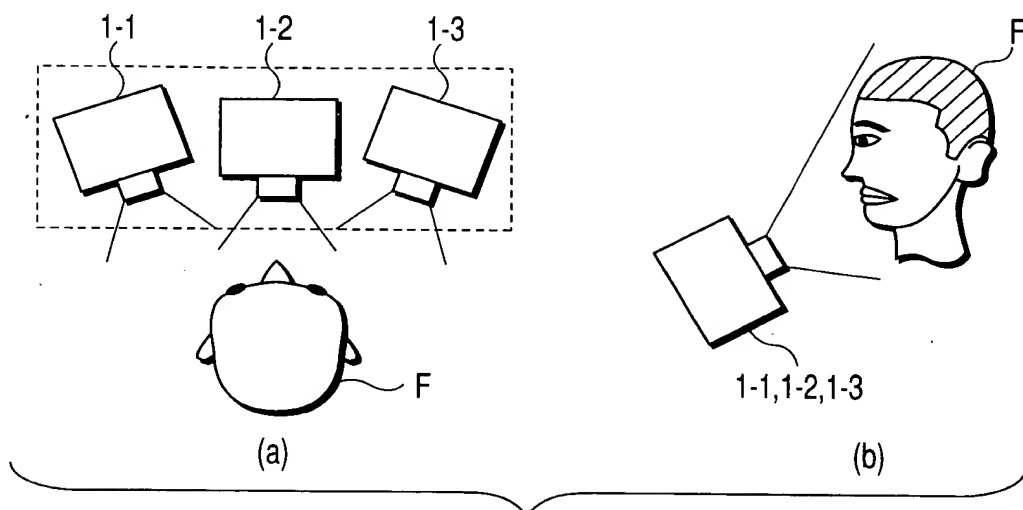


FIG. 9

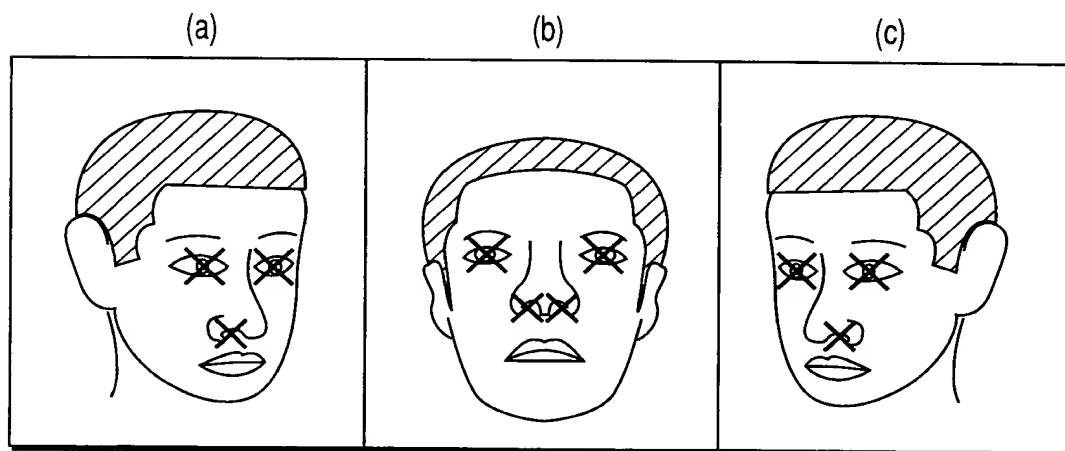


FIG. 10

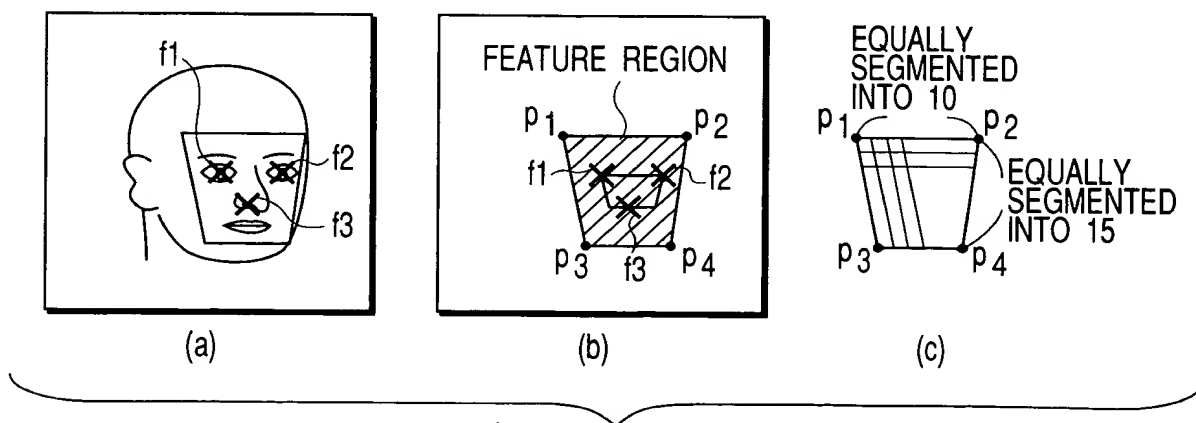


FIG. 11

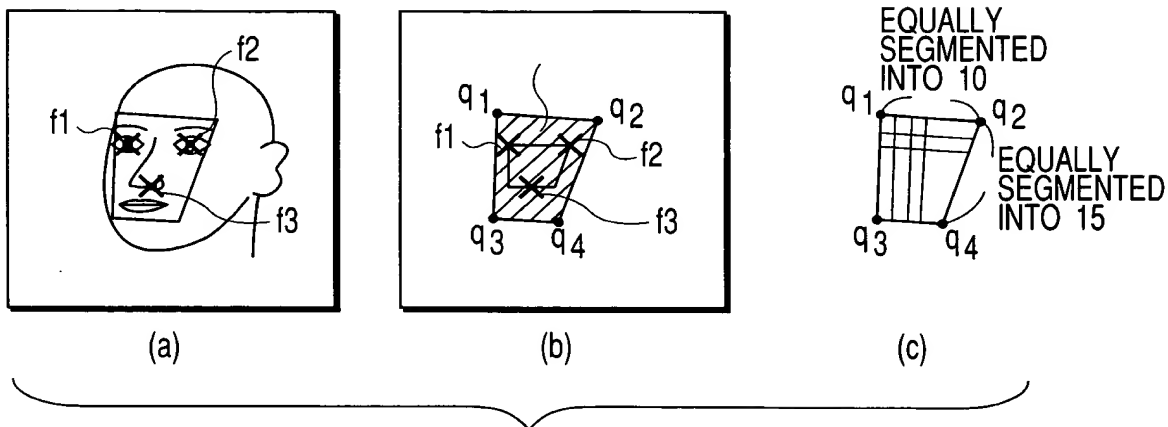


FIG. 12

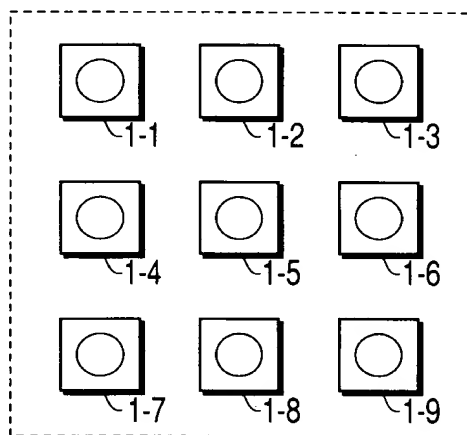


FIG. 13

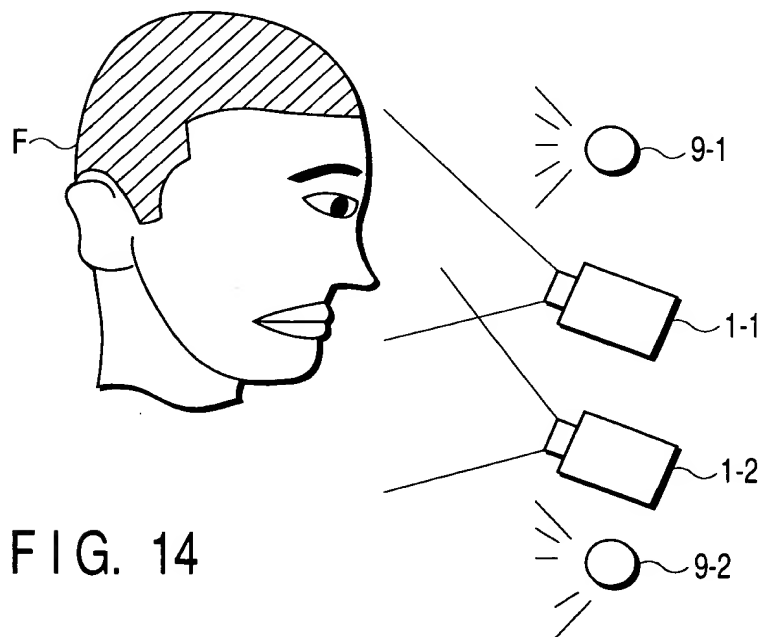


FIG. 14

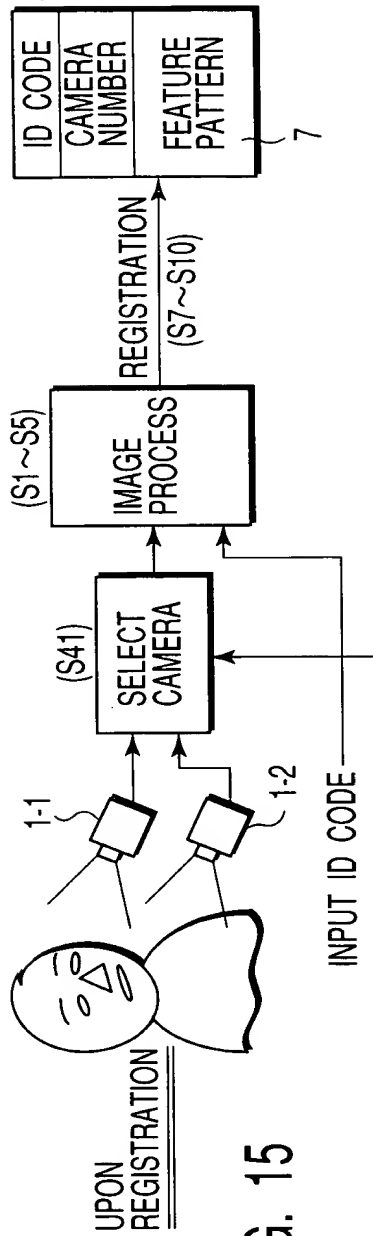
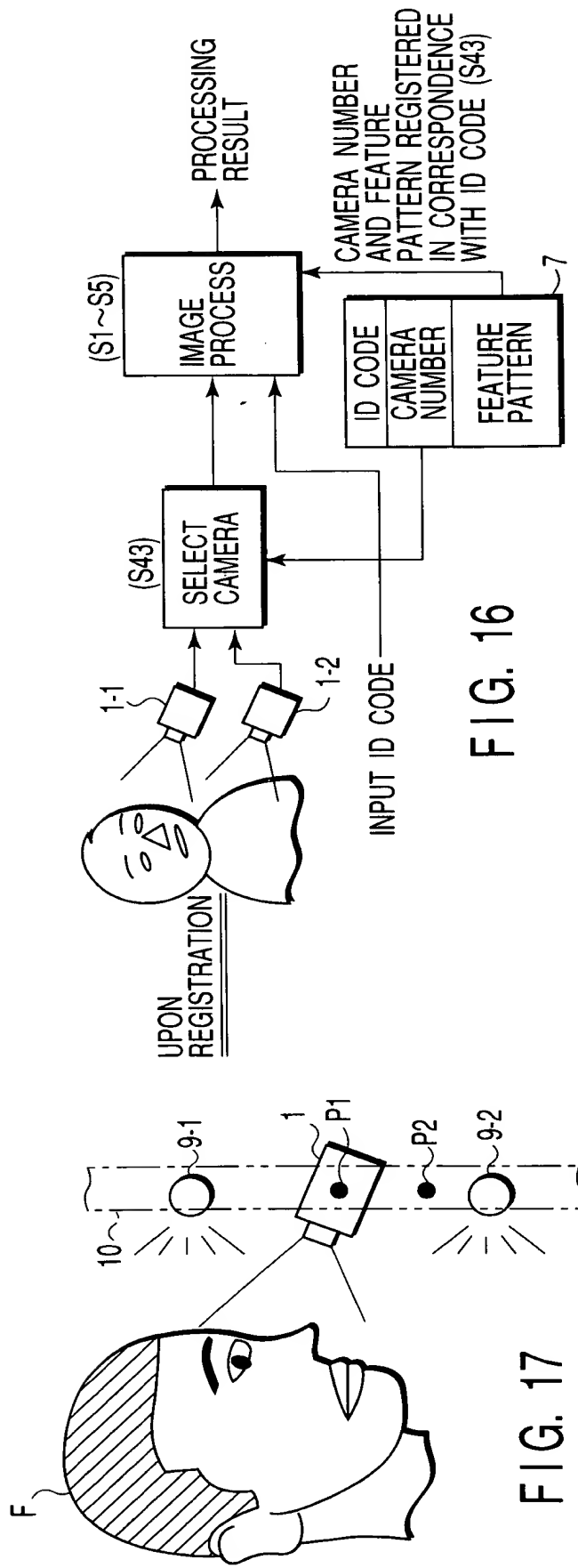


FIG. 15



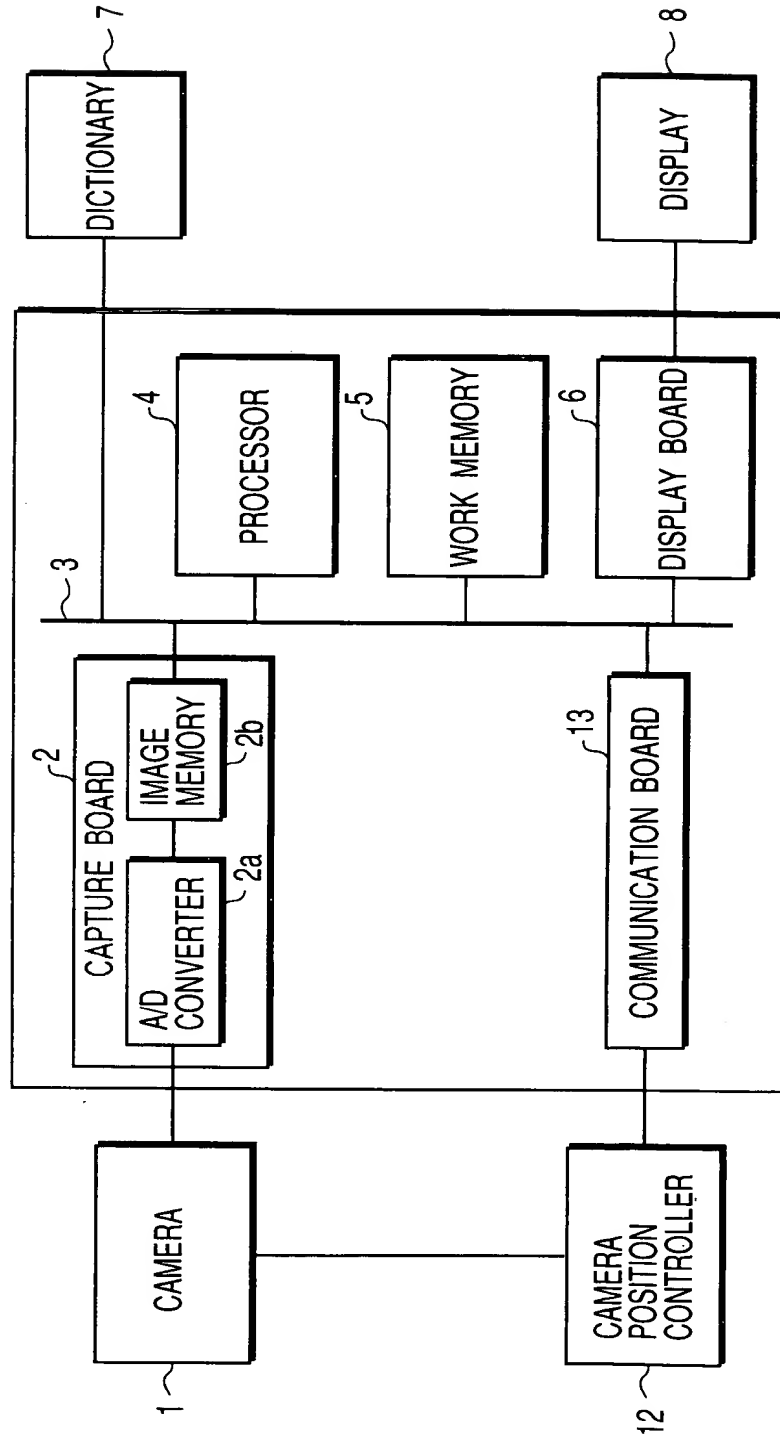


FIG. 18



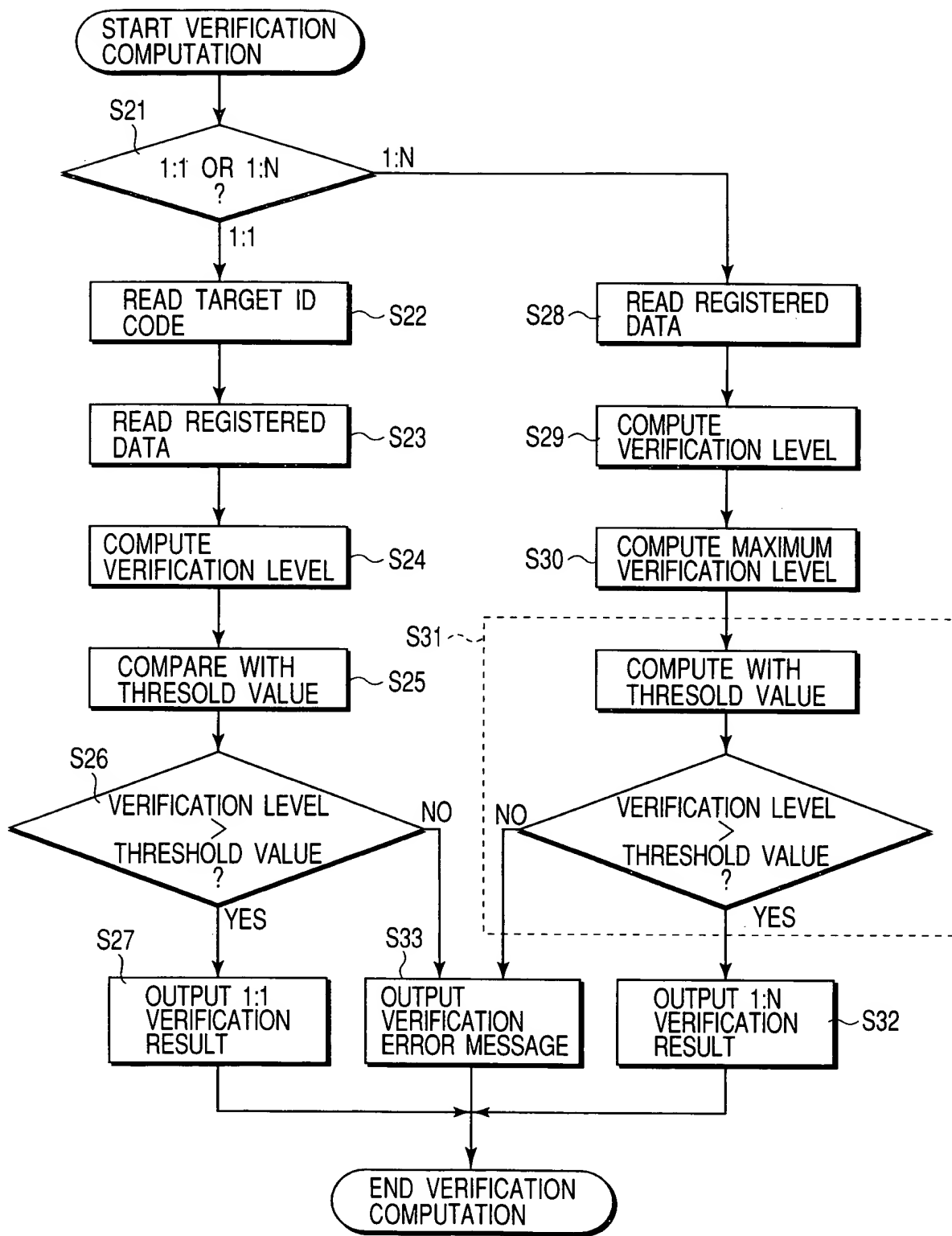


FIG. 19